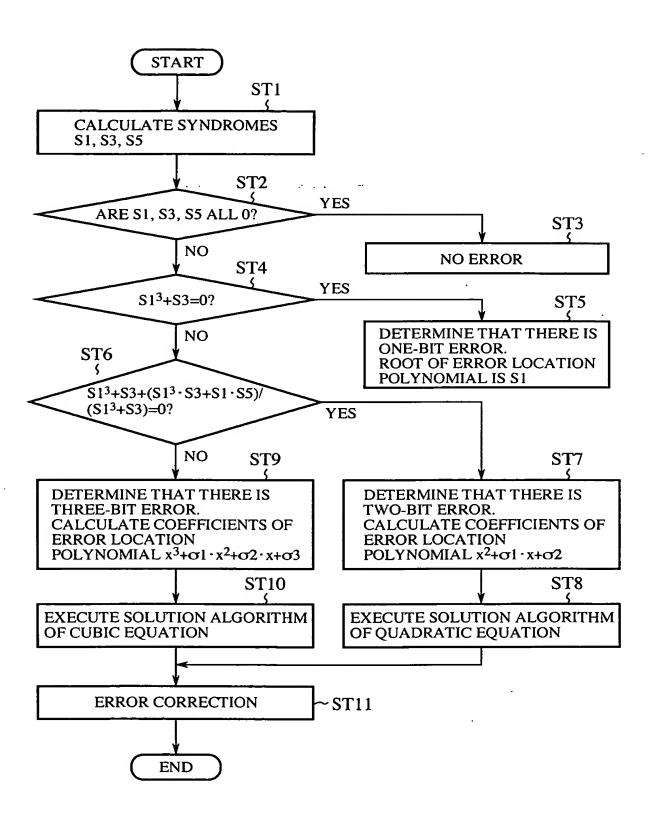
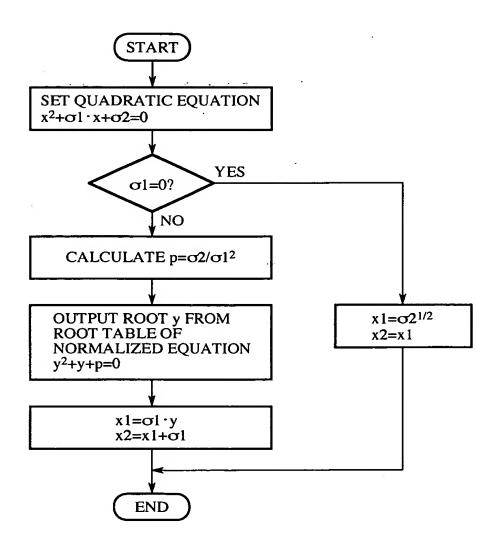
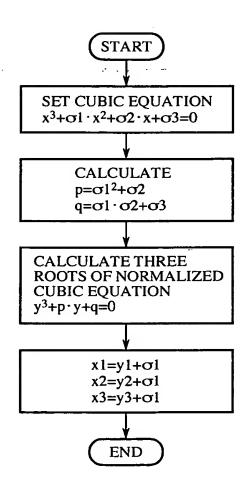


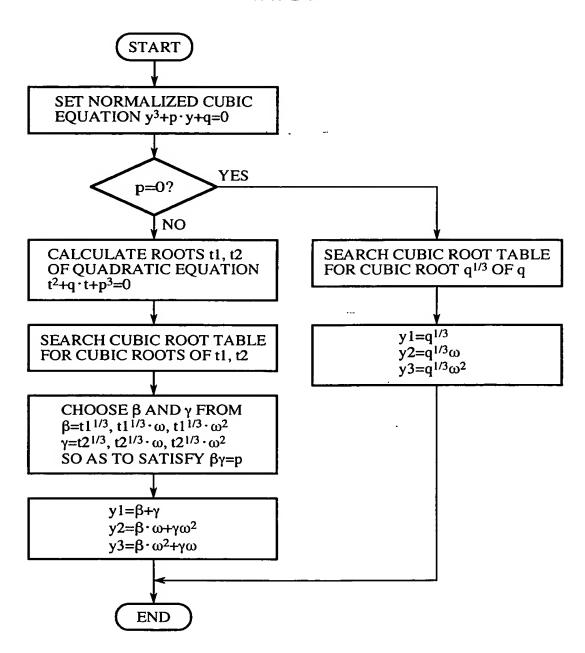


FIG.1









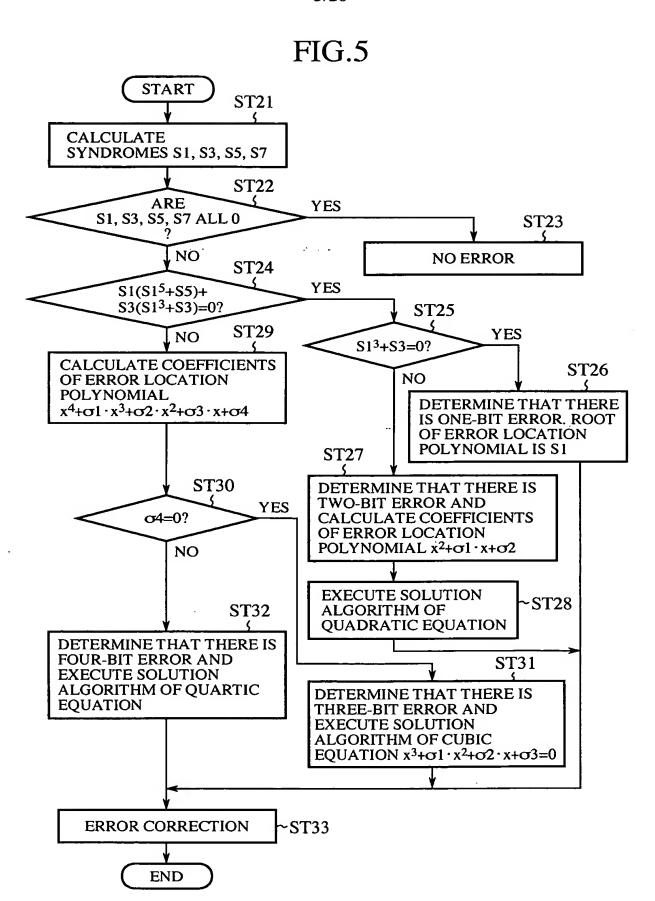
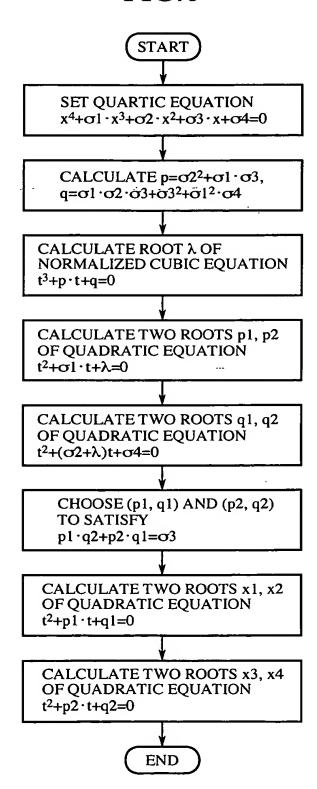




FIG.6



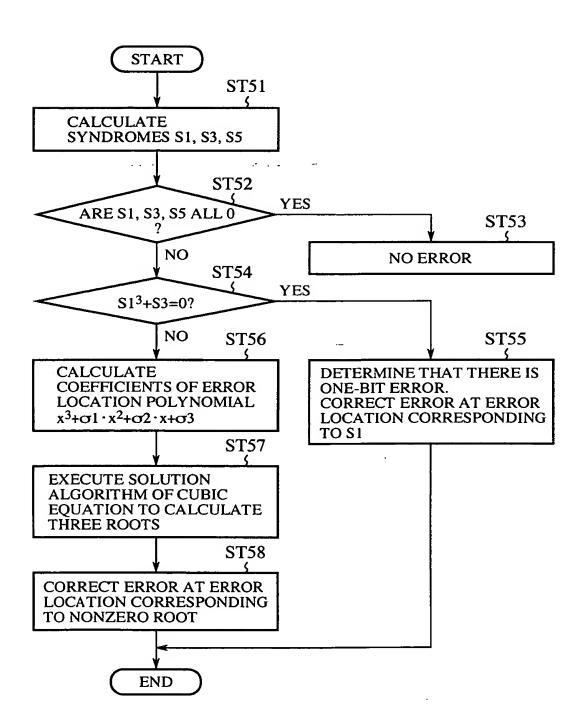
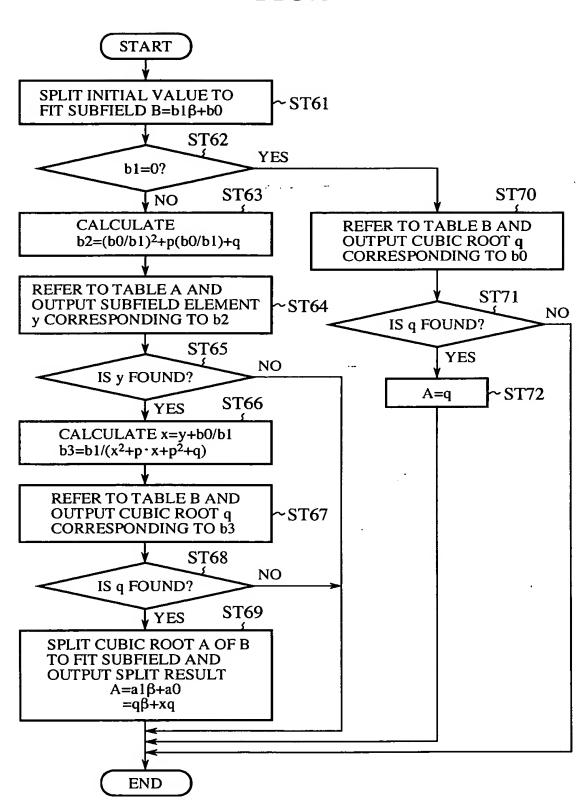


FIG.8



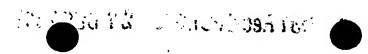


FIG.9

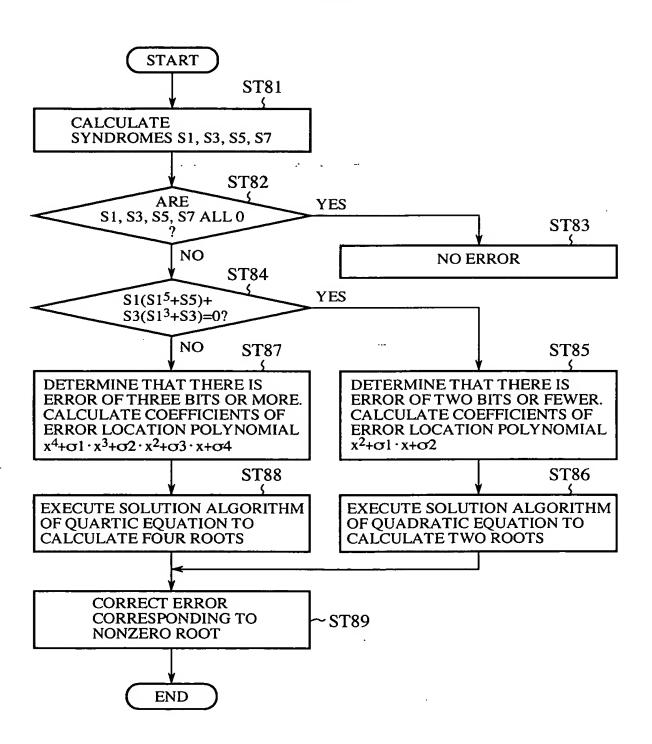


FIG.10

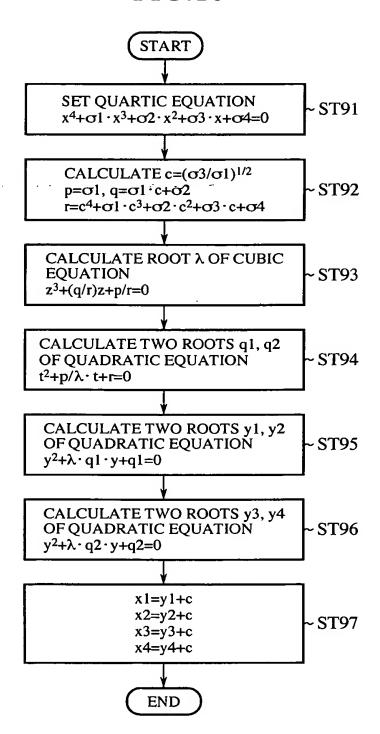
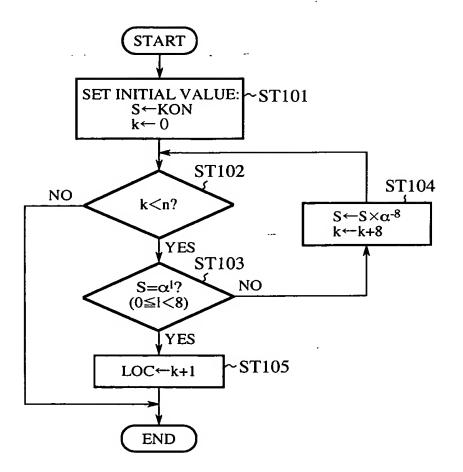
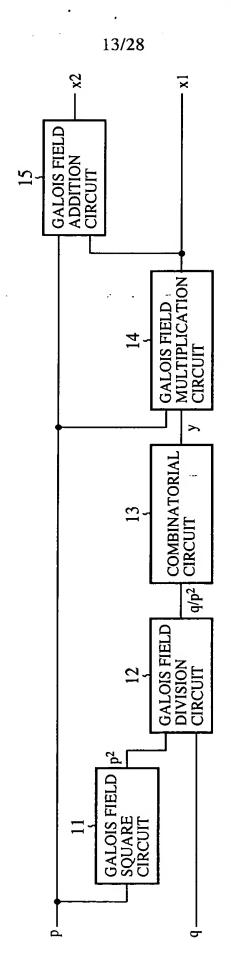


FIG.11



CORRECTING ERROR LOCATION TABLE **ERROR LOCATION** POLYNOMIAL SOLUTION CIRCUIT **ERROR LOCATION** POLYNOMIAL GENERATING CIRCUIT DELAY CIRCUIT ERROR BIT COUNT ESTIMATING SYNDROME GENERATING CIRCUIT

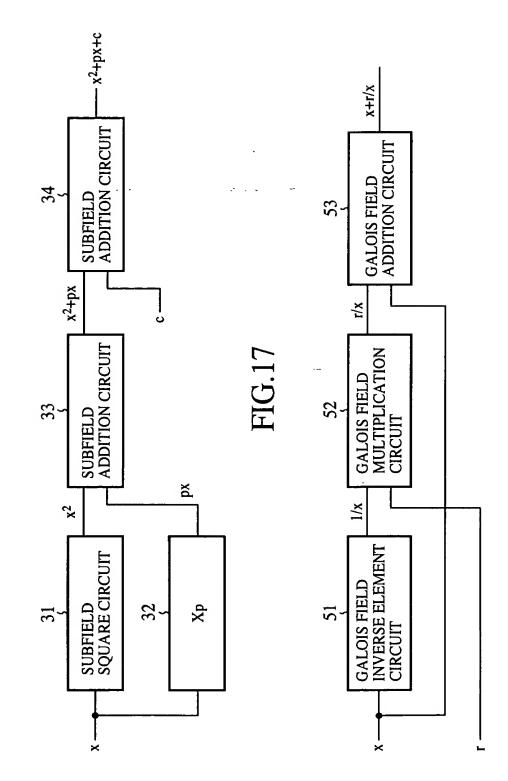
IG.13





BASE REVERSE TRANSLATING CIRCUIT x^2+px+p^2+q xd SUBFIELD MULTIPLICATION TRANSLATING CIRCUIT 29 CIRCUIT ADDITION CIRCUIT SUBFIELD TABLE B) LOOKUP TABLE 25 **b3** LOOKUP (TABLE TABLE SUBFIELD DIVISION CIRCUIT 24 TRANSLATING CIRCUIT 23 SUBFIELD DIVISION CIRCUIT þl BASE TRANSLATING CIRCUIT В

FIG. 15





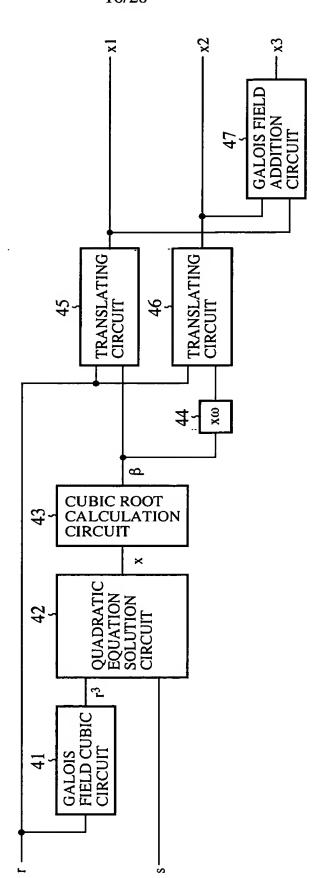


FIG.18

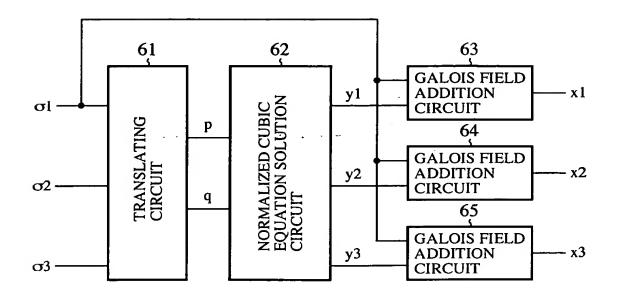
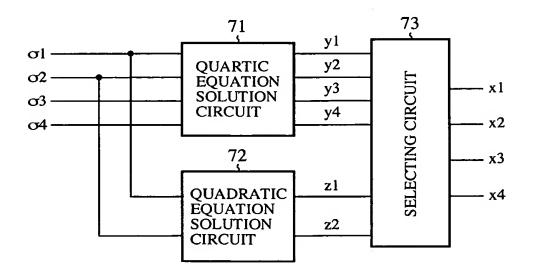


FIG.19



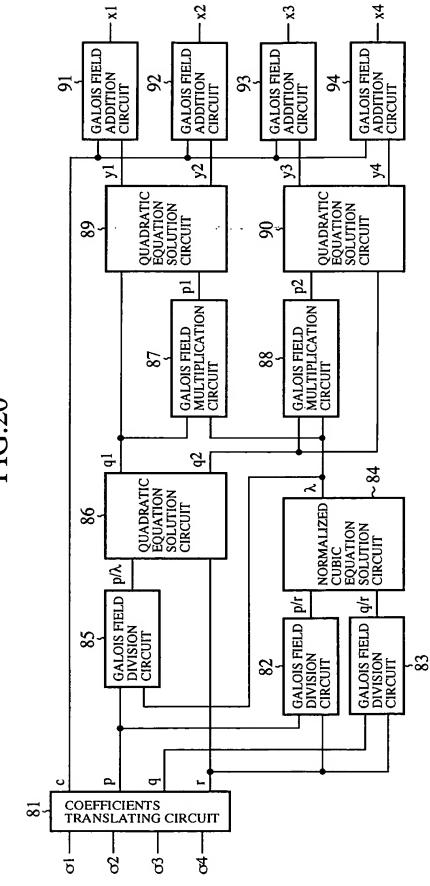


FIG 20

FIG.21

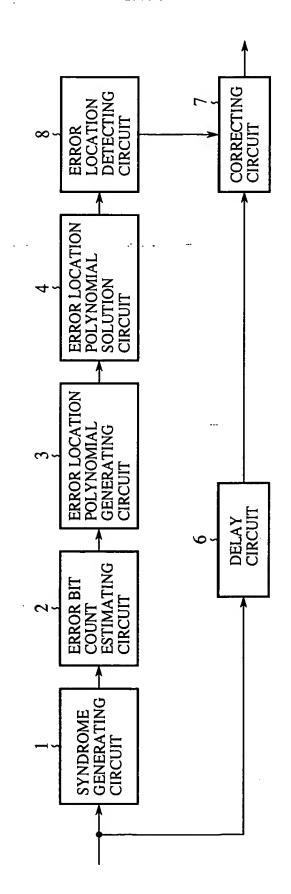


FIG.22

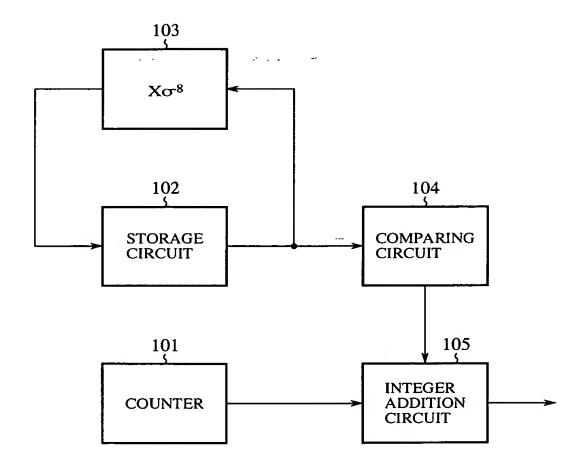


FIG.23

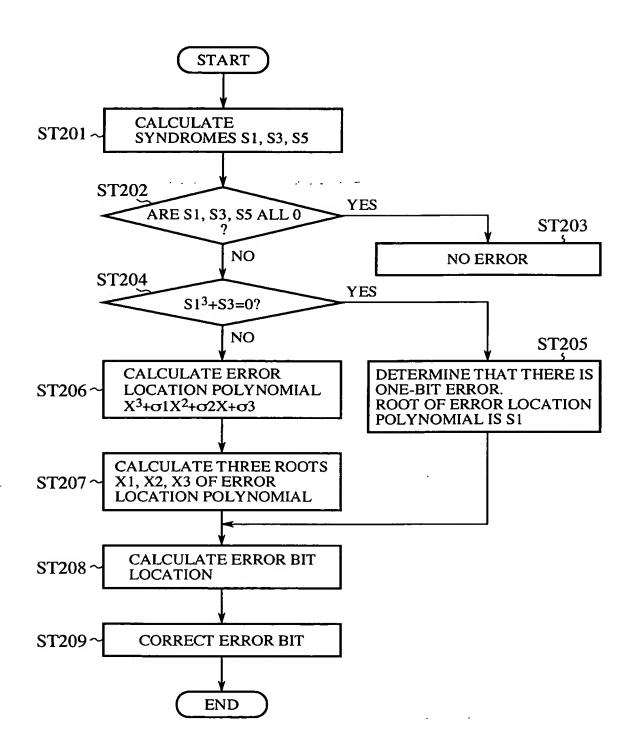


FIG.24

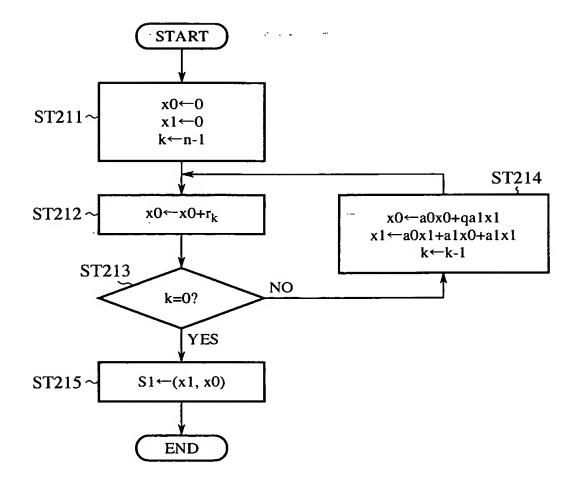


FIG.25

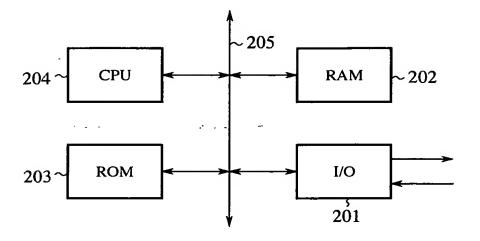
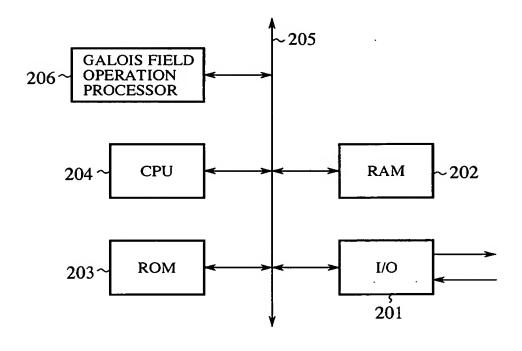
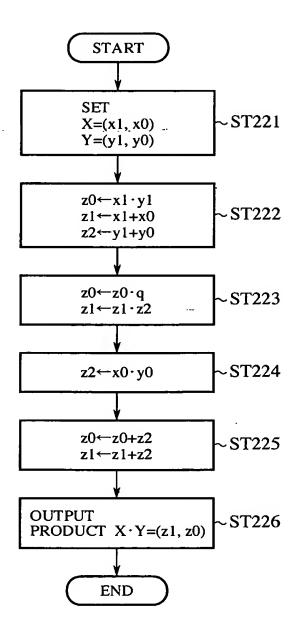
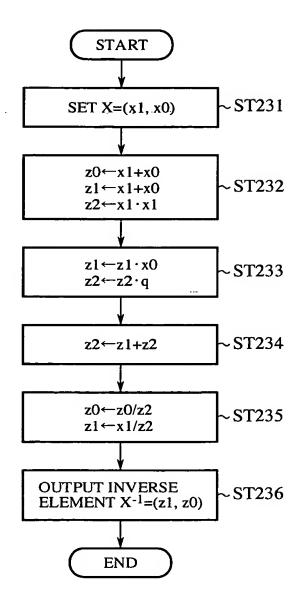


FIG.26







4

FIG.29

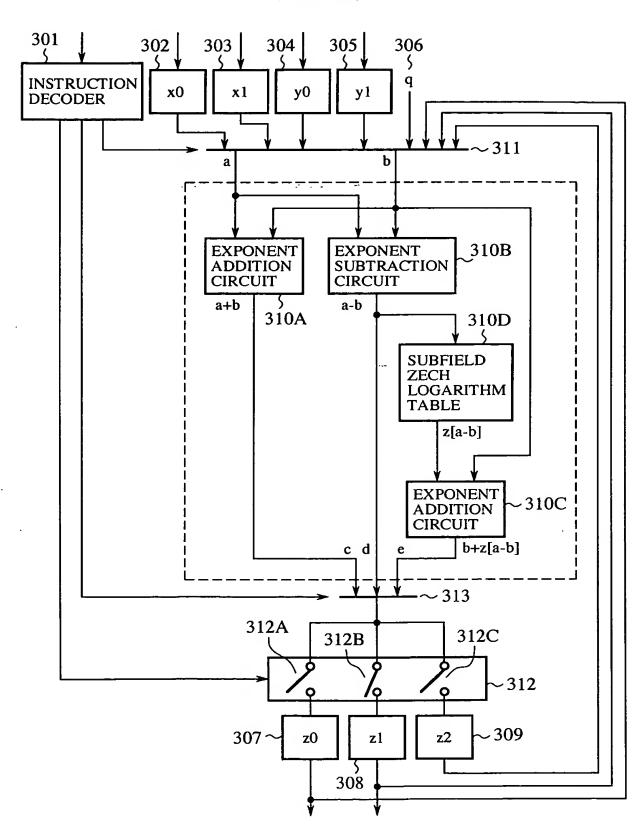


FIG.30

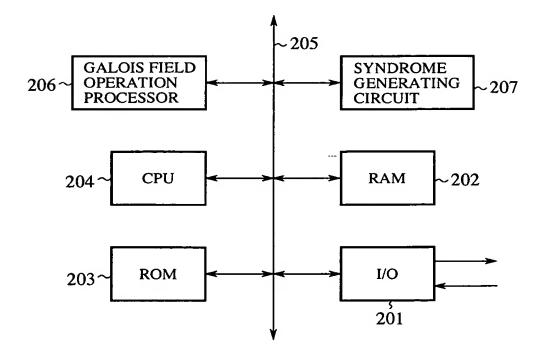


FIG.31

